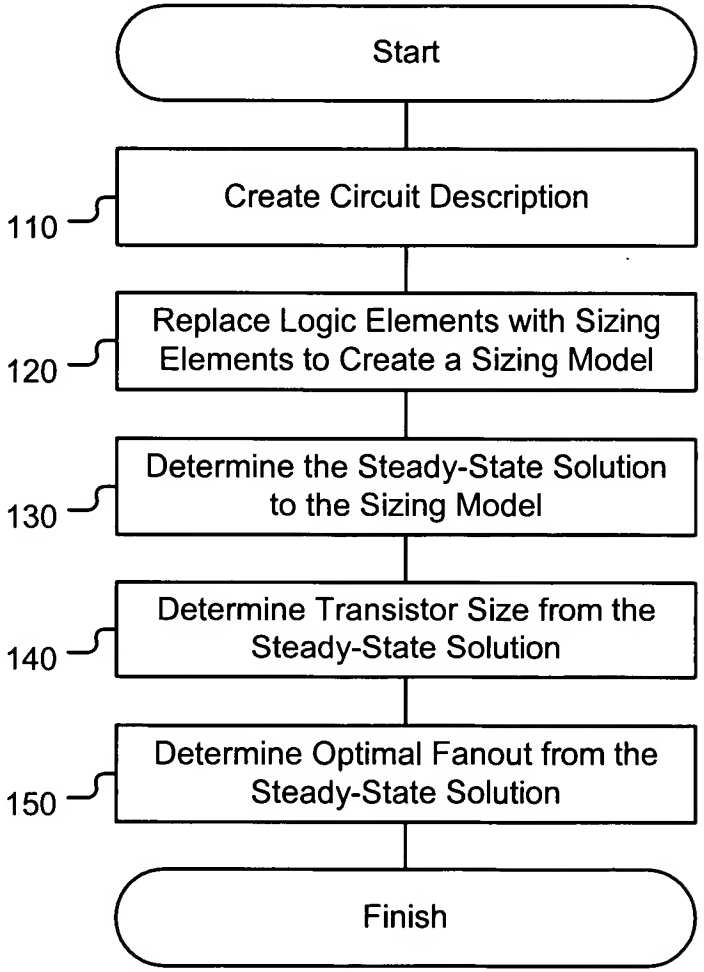
Start
Create Circuit Description
110
Replace Logic Elements with Sizing Elements to Create a Sizing Model
120
Determine the Steady-State Solution to the Sizing Model
130
Determine Transistor Size from the Steady-State Solution
140
Determine Optimal Fanout from the Steady-State Solution
150
Finish

Figure 1

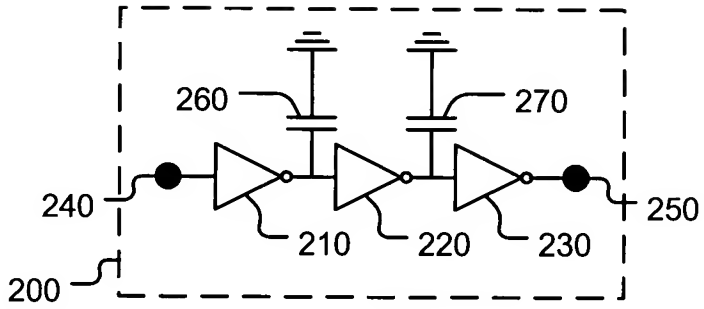

Figure 2

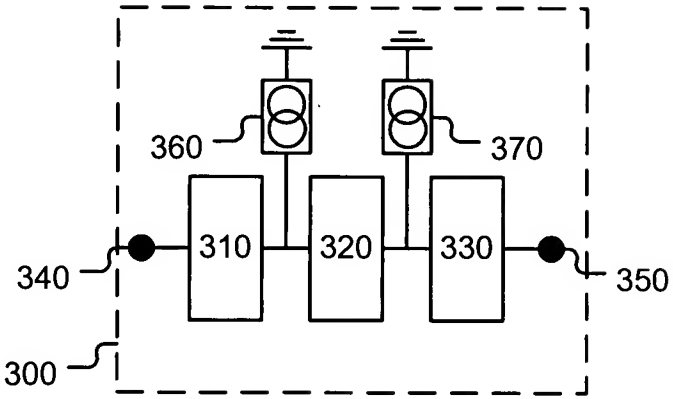

Figure 3

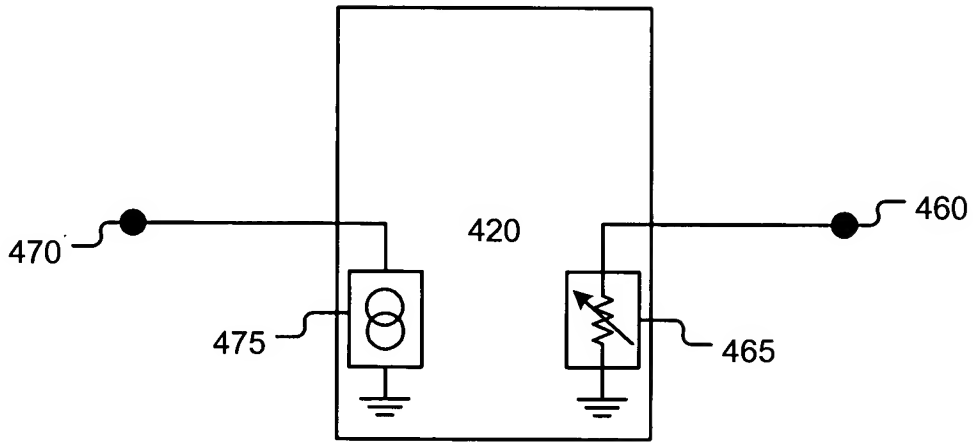

Figure 4

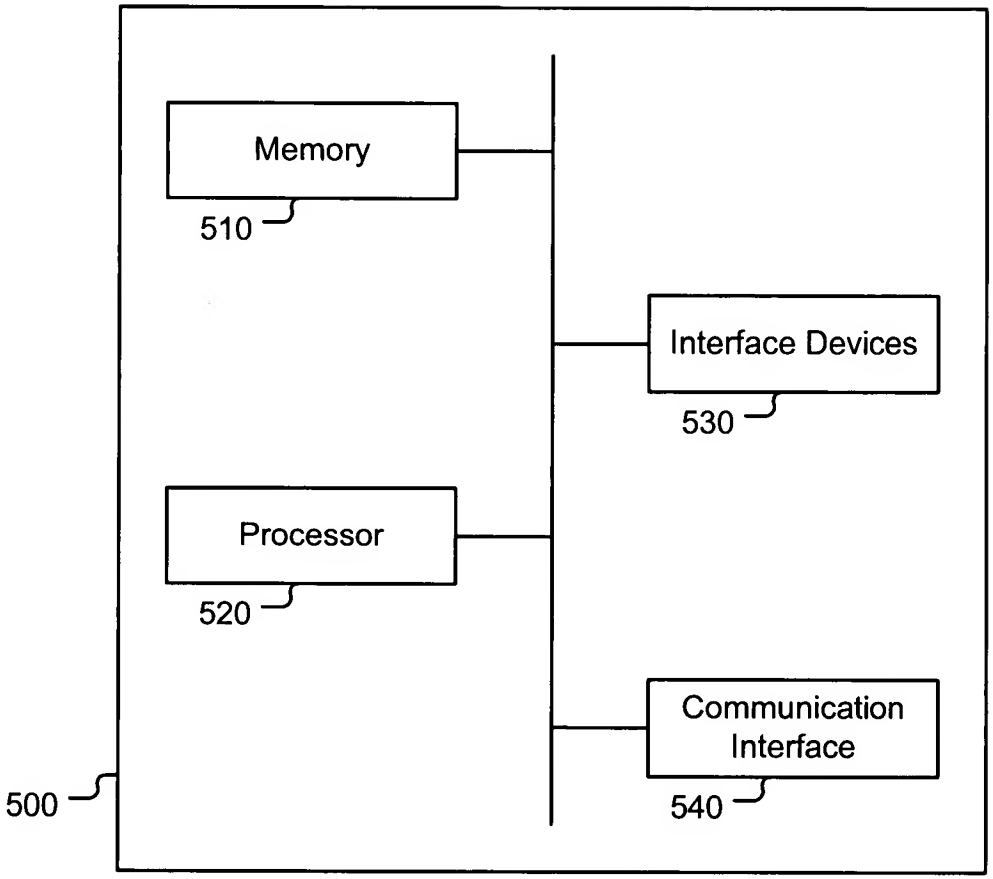
Memory
510
Interface Devices
530
Processor
520
Communication Interface
540
500

Figure 5